



PATENT

Date of Notice
of Allowance : April 4, 2005

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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Signature

Confirmation No. 9113

Applicant : Janardhanan S. Ajit
Application No. : 10/621,005
Filed : July 16, 2003
Title : SUB-MICRON HIGH INPUT VOLTAGE TOLERANT INPUT
OUTPUT (I/O) CIRCUIT
Grp./Div. : 2816
Examiner : Tuan Thieu Lam
Docket No. : 50716/RJP/B600

COMMENTS ON STATEMENT OF REASONS FOR ALLOWANCE

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Commissioner for Patents
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Post Office Box 7068
Pasadena, CA 91109-7068
June 20, 2005


Commissioner:

Reasons for allowance are only warranted in instances in which "the record of the prosecution as a whole does not make clear his or her reasons for allowing a claim or claims." (37 CFR § 1.104(e))

Applicant believes the Examiner's stated reasons for allowance are unnecessary. The Applicant does not necessarily agree with each statement in the reasons for allowance. While the Applicant agrees that the claims are allowable, applicant does not acquiesce with each statement in the reasons for allowance, that patentability requires each stated feature exactly as expressed by the Examiner, nor that each stated feature is required for patentability.

Respectfully submitted,
CHRISTIE, PARKER & HALE, LLP

By


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